**ASSIGNMENT-3**

1. (a) What is the importance of port-C of 8255 in BSR mode.

(b) Write a control word to configure 8255 in mode 0 with PCL and PCU as I/p ports. PA and PB as o/p ports. Write a segment of program to read the data from PCL on to port PA and PCU on to port PB.

(c) An 8255 has a system base address of FFF9H. What are the system address for the three ports and the control register for this 8255.

2. Draw the block diagram for configuring 8086 in MAX mode. Explain in detail how the control signals are generated in it.

3. (a) Draw and explain the timing diagram for 8086 maximum mode input operation.

(b)Differentiate between hard ware and software interrupts. Also explain type2 interrupt.

4. (a)Draw the diagram for 8086 interrupt response and interrupt pointer table.

(b)If an interrupt has been requested, 8086 respond to the interrupt by performing series of steps. Mention those steps.

5. (a) List some of the features of INTEL 8259.

(b) Give the flowchart form of initialization to program 8259 and explain?

(c) What is the function of GATE signal in the 8254 timer?